## PATENT ABSTRACTS OF JAPAN

(11)Publication number:

08-250308

(43) Date of publication of application: 27.09.1996

(51)Int.CI.

H01C 7/04

H01C 7/18

(21)Application number : **07-049663** 

(71)Applicant: MITSUBISHI MATERIALS

**CORP** 

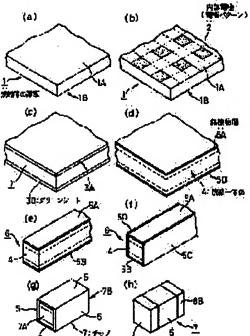
(22)Date of filing:

09.03.1995

(72)Inventor:

MAEDA YASUTAKA NAKAJIMA HIROAKI KOSHIMURA MASAMI

## (54) MULTILAYERED CHIP THERMISTOR AND MANUFACTURE THEREOF



整款, (57)Abstract:

PURPOSE: To provide a multilayered chip thermistor having a high dimensional accuracy wherein inner electrodes are little dislocated by baking and dispersion of resistance values is little.

CONSTITUTION: External electrodes 8a and 8b are formed on both end faces of a thermistor base 7 made by sintering and unifying a multilayered body composed of a baked ceramic body 1 and ceramic green sheets 3A and 3B through electrodes thereon. Since the sintered body little reshrink at baking, the area of the electrodes formed on its surface, the distance between the opposed electrodes, and the distance of other electrodes formed on the same surface little change. The influence of the shrinkage of the green sheets on the resistance value is little and dispersion of the resistance values is avoidable. Since the sintered body little shrink but only the

green sheets at baking, the total shrinkage is low, dimensional dispersion is little and dimensional accuracy is high.

**LEGAL STATUS** 

[Date of request for examination]

31.03.1999

[Date of sending the examiner's decision of 02.10.2001 rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number] 3331807 [Date of registration] 26.07.2002 [Number of appeal against examiner's 2001-19734 decision of rejection]

[Date of requesting appeal against examiner's decision of rejection]

02.11.2001

[Date of extinction of right]

Copyright (C); 1998,2003 Japan Patent Office